MULTIMEDIA UNIVERSITY

STUDENT ID NO

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MULTIMEDIA UNIVERSITY

FINAL EXAMINATION

TRIMESTER 2, 2022/2023

EEL1166 – CIRCUIT THEORY

(All sections / Groups)

14 JULY 2023
9.00 a.m. – 11.00 a.m.
(2 Hours)

INSTRUCTIONS TO STUDENTS

1. This question paper consists of **FIVE** pages including cover page with **FOUR** questions only.

2. Answer **ALL** questions and print all your answers in the answer booklet provided.

3. All questions carry equal marks and the distribution of the marks for each question is given.

Question 1

(a) Use **superposition theorem,** determine the voltage, V_{RL} in the circuit shown in **Figure Q1(a).**
(Note: Re-sketch the circuit diagram and show your calculation for each step of the superposition theorem.) [9 marks]

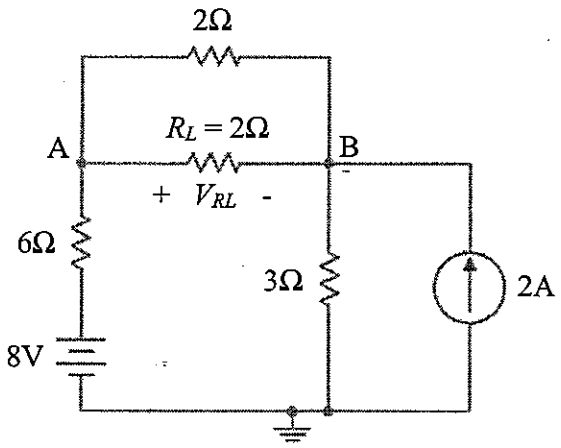

Figure Q1(a)

(b) For the same circuit shown in **Figure Q1(a),** use **source-conversion theorem** to convert the current source into a voltage source. Then, apply **Thevenin's theorem** on the load resistor, R_L in order to calculate the Thevenin's resistance, R_{TH} and Thevenin's voltage, V_{TH} between nodes **A** and **B.** You are **not required** to use superposition theorem to answer the question.
(Note: Re-sketch the circuit diagram and show your calculation for each step of the source-conversion and Thevenin's theorem.) [10 marks]

(c) Based on the answer obtained in part (b), draw the Thevenin's equivalent circuit and find the voltage, V_{RL}. Assume we need to change the load resistor, R_L in the circuit frequently and analyse the changes of voltage across the load resistor, what is the advantage of using the **Thevenin's theorem** for circuit analysis?
[6 marks]

Continued...

Question 2

(a) Determine the node voltages, v_1 and v_2 in the circuit shown in **Figure Q2(a)** using **nodal analysis**. Then, calculate the voltage across the 5Ω resistor, $V_{5\Omega}$ using the node voltages. [11 marks]

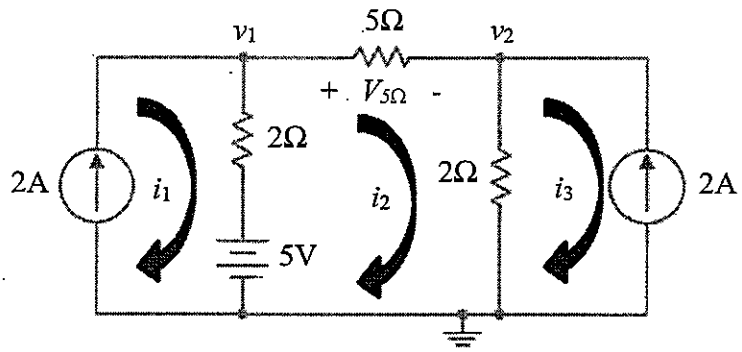

Figure Q2(a)

(b) For the same circuit shown in **Figure Q2(a)**, use **mesh analysis** to determine the mesh currents, i_1, i_2 and i_2. Then, calculate again the voltage across the 5Ω resistor, $V_{5\Omega}$ using the mesh currents. [9 marks]

(c) Is the voltage across the 5Ω resistor, $V_{5\Omega}$ obtained in parts (a) and (b) the same value? Which of the circuit analysis performed in parts (a) and (b) is easier to find the voltage across the 5Ω resistor? Why? [5 marks]

Continued...

Question 3

(a) The circuit shown in **Figure Q3(a)** has a sinusoidal source supply with an angular frequency of 50Hz.

(i) What is the relationship between complex power and average power? Why is it important to measure the average power when constructing a circuit? [5 marks]

(ii) Total up all the impedance, Z_T of the circuit. [8 marks]

(iii) Calculate the total complex power, S, and real power, P of the circuit. [5 marks]

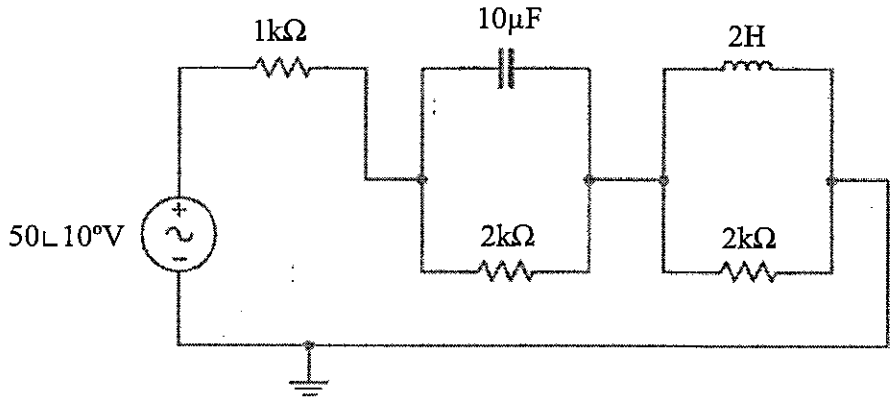

Figure Q3(a)

(b) A parallel resonance circuit has an unknown resistance of R Ω, an unknown capacitance of C F and an inductance of 10mH. If it requires a resonant frequency at 10kHz and a high quality factor of 25. Calculate the following:

(i) capacitance, C and resistance, R. [4 marks]

(ii) half-power bandwidth, B in rad/s. [3 marks]

Continued...

Question 4

(a) A circuit shown in **Figure Q4(a)** has a switch, **S1** connected to **A** for a long time prior to $t = 0$. At $t \geq 0$s, it changes its connection to terminal **B** and then, at $t \geq 1$s, it switches back to terminal **A** again.

(i) Analyse the circuit by deriving the mathematical expression of the capacitor voltage, $v_C(t)$ for $1\text{s} > t \geq 0\text{s}$ and for $t \geq 1\text{s}$. [11 marks]

(ii) Obtain the voltage, v_R at $t = 0.5$s and $t = 1.5$s. [5 marks]

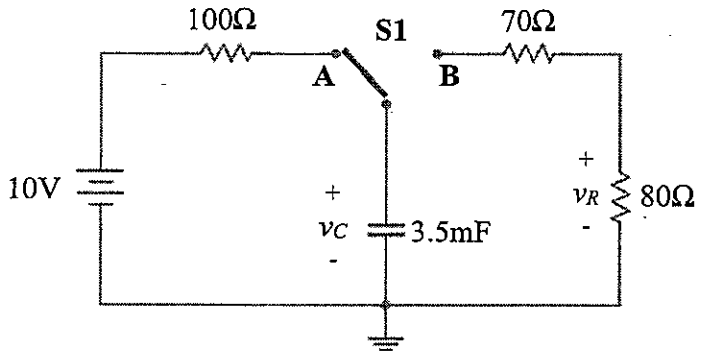

Figure Q4(a)

(b) A source-free **series** *RLC* circuit has a differential current equation as follows:

$$\frac{d^2i}{dt^2} + 2\frac{di}{dt} + 10 = 0,$$

(i) Form the characteristic equation of the circuit. [2 marks]

(ii) Is the circuit exhibits a critically damped, overdamped or underdamped output response? [5 marks]

(iii) How does the output response obtained in part (b)(ii) affect the current flow of the *RLC* circuit? [2 marks]

End of Paper